



Product Change Notification

110284 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 110284 - 00
Change Title: Boxed Intel® Core™ i5-661 Processor, PCN 110284-00, Product Design, C2 to K0 Stepping Conversion
Date of Publication: October 22, 2010

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Nov 8, 2010
Date of First Availability of Post-Conversion Material:	Nov 8, 2010

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel is initiating a C-2 to K-0 stepping conversion for the Boxed Intel® Core™ i5-661 Processor where it will undergo the following changes:

- . New S-spec and MM number for the converting product
- . Extended CPUID will change from 0x00020652 to 0x00020655
- . Host RevID will change from 0x12 to 0x18
- . K0 stepping package is pin compatible with C2 stepping package
- . Adds Processor Context ID (PCID) support

Customer Impact of Change and Recommended Action:

Intel anticipates no change to customer platforms designed to Intel guidelines. Minimal re-qualification and/or validation are expected for features already supported on the C2 stepping. Boxed Intel® Core™ i5-661 Processor on K0 stepping will require a BIOS update. With the BIOS update implemented both C2 and K0 stepping processors will be supported. For customers implementing their own BIOS, it is necessary to incorporate the latest Microcode Update (MCU) to support the K-0 stepping. Please contact your local Intel representative for further technical questions. Customer should update to the latest Intel Integrated Graphics drivers for K-0 step processors.

Customers who want to continue using the Intel® Stable Image Platform Program (Intel® SIPP) drivers with K-0 step processors should confirm BIOS support for Compatibility Rev ID (CRID). Customers should be ready to receive a combination of C2 stepping material and K0 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" as shown in the milestones above.

Products Affected / Intel Ordering Codes:

Boxed Intel® Core™ i5-661 Processor									
Processor #	GHz	Pre Conversion Material				Post Conversion Material			
		Product Code	S-Spec	MM#	Stepping	Product Code	S-Spec	MM#	Stepping
i5-661	3.33	BX80616I5661	S LBNE	904942	C2	BX80616I5661	S LBTB	908514	K0
i5-661	3.33	BXC80616I5661	S LBNE	904943	C2	BXC80616I5661	S LBTB	908523	K0

PCN Revision History:

Date of Revision:

October 22, 2010

Revision Number:

00

Reason:

Originally Published PCN