



Product Change Notification

103415 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geography location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

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Product Change Notification

Change Notification #: 103415 - 00
Change Title: DI0408LSAR2EU, PCN 103415-00, FYI,
Change to correct exceeding the PCI board
envelope dimension and change to avoid
possibility of incorrect booting.
Date of Publication: August 08, 2003

Type of Change Notification:
FYI - (For Your Information)

Key Characteristics of the Change:
Product Design

Forecasted Key Milestones:

Date of First Availability of Post-Conversion Material:	Sep 19, 2003
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The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Change to correct exceeding the PCI board envelope dimension that could result in the PCI board assembly touching the adjacent PCI board assembly. The solution consists of replacing the existing power cable strain relief screw and nut and adding a split lock washer.
Change to remove resistor from location R438 and install in location R338. These resistors are used to determine FLASH bus width and could result in incorrect booting.

Customer Impact of Change and Recommended Action:

This change is a design enhancement and has no impact on units in the field.

This change has been thoroughly evaluated to ensure that it does not have any negative functional implications to our customer.

At the customer's discretion, Intel will accept returns of existing product for re-work and return to incorporate these changes through the DRA process.

Customers should modify their databases in order to place orders against the new order codes. See details below.

Products Affected / Intel Ordering Codes:

Board Products Table

Affected Pre Change Product Code	Pre-Change MM#	Post Change Product Code	Post-Change MM#
DI0408LSAR2EU	854646	DI0408LSAR2EU	855831
DI0408LSAR2EU	852784	DI0408LSAR2EU	855831

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

August 8, 2003

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Originally Published PCN