



Product Change Notification

110040 - 01

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 110040 – 01
Change Title: Intel® Xeon® Processor L3406, PCN 110040-01, Product Design, C2 to K0 Stepping Conversion, Reason for Revision: Update Milestone Dates & Processor Frequency
Date of Publication: July 23, 2010

Key Characteristics of the Change:
 Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Aug 02, 2010
Date of Qualification Data Availability:	Jul 05, 2010
Date Customer Must be Ready to Receive Post-Conversion Material:	Oct 25, 2010
Date of First Availability of Post-Conversion Material:	Oct 25, 2010

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

The ONLY changes made to this PCN are for the two Milestone dates listed below and processor frequency from 2.00GHz to 2.26GHz in the Products Affected / Intel Ordering Codes table.

Forecasted Key Milestones:	From	To
Date of Samples Availability:	Jun 28, 2010	Aug 02, 2010
Date of First Availability of Post-Conversion Material:	Jul 05, 2010	Oct 25, 2010

ALL OTHER DATES AND INFORMATION IN THIS PCN REVISION REMAIN THE SAME.

ALL VERBIAGE BELOW THIS LINE IS FROM THE ORIGINAL PCN:

Intel is initiating a C-2 to K-0 stepping conversion for the Intel® Xeon® Processor L3406 where it will undergo the following changes:

- New S-spec and MM numbers for the converting products
- Extended CPUID will change from 0x00020652 to 0x00020655
- Host RevID will change from 0x12 to 0x18
- K0 stepping package is pin compatible with C2 stepping package
- Adds Processor Context ID (PCID) support

Customer Impact of Change and Recommended Action:

Intel anticipates no change to customer platforms designed to Intel guidelines. Minimal re-qualification and/or validation is expected for features already supported on the C2 stepping.

Intel® Xeon® Processor L3406 on K0 stepping will require a BIOS update. With the BIOS update implemented both C2 and K0 stepping processors will be supported. For customers implementing their own BIOS, it is necessary to incorporate the latest Microcode Update (MCU) to support the K-0 stepping. Please contact your local Intel representative for further technical questions.

Customers should be ready to receive a combination of C2 stepping material and K0 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" as shown in the milestones above.

Products Affected / Intel Ordering Codes:

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
L3406	2.26 GHz	CM80616005010AA	S LBQQ	905444	Q 4DN	907910	CM80616005010AA	S LBT8	907923

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

June 28, 2010

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Originally Published PCN

July 23, 2010

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Update Milestone Dates and Processor Frequency