



Product Change Notification

107115 - 02

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 107115 - 02
Change Title: SR9000MK4U, PCN 107115-02, Product Design, Change to attention document, power button hole diameter, and FPGA logic change. Lithium Perchlorate Notification, Reason for Revision: Add California Lithium Perchlorate Notification.
Date of Publication: January 04, 2007

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Jan 15, 2007
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Description of Change to the Customer:

Reason for Revision: Add California Lithium Perchlorate Notification.

This PCN contains the following changes subsequently will be referred to as 1), 2), 3), or 4).

1) Added notice to the Attention Document that when using Windows OS, system admins need to disable write combining notice, additional details listed in Action Alert #854-01.

2) Increased diameter of the front panel sheet metal button guide holes by 1mm to reduce small possibility of depressed buttons becoming stuck.

3) FPGA OR gate changed to an AND gate to guarantee PWROK line Low status when MAINPWGD signal is low to ensure server will successfully complete POST. Prior to this change, there exists a rare occasion for some parts that may have slipped past screening to not power on when the power button is pressed once under high ambient temperatures in the range of +35 degrees Celsius. The lack of the PWROK line low during POST will most assuredly result in a system not completing POST, and needing to be power cycled to boot. No customers should have received unscreened parts and thus should not see any change in the expected product performance as a result of this change.

4) All systems with motherboards containing Lithium batteries must now contain a Lithium Perchlorate Notification sheet as per California environmental regulatory requirements. This is a documentation addition only.

Customer Impact of Change and Recommended Action:

1) Intel requires customers follow instructions found in the attention document and the action alert #854-01 for disabling write combining in the Windows OS.

2), 3), and 4) Intel anticipates no customer impact to SR9000MK4U.

4) is a regulatory documentation change only.

Products Affected / Intel Ordering Codes:

Pre Change Product Code	Pre Change MM#	Pre Change TA	Post Change TA
F9000MKBRD 886623	886623	D73874-001	D73874-002
SR9000MK4U 885580	885580	D68846-001	D68846-002
F9000OPPNL 886626	886626	D73890-001	D73890-002

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

December 19, 2006

00

Originally Published PCN

December 20, 2006

01

Removing BULK SKU's not visible to customers.

January 4, 2007

02

Add California Lithium Perchlorate Notification.