



# Product Change Notification

## 107108 - 01

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

**Europe Email:** [eccb@intel.com](mailto:eccb@intel.com)

**Japan Email:** [jccb.ijkk@intel.com](mailto:jccb.ijkk@intel.com)

Copyright © Intel Corporation 2007. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



# Product Change Notification

**Change Notification #:** 107108 - 01  
**Change Title:** S5000XVN,PCN 107108-01, Product Design, Class 1: Hardware, Software and regulatory document changes, Reason for Revision: Adding FBDIMM resistor change  
**Date of Publication:** January 12, 2007

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Jan 19, 2007
---	--------------

## Description of Change to the Customer:

### Reason for Revision: Adding FBDIMM resistor change

- Changing revision of GTL to improve circuitry robustness
- Removing flash component at location U1F3 that is not utilized currently, nor forecast to be utilized
- Replace BIOS version R0059 with BIOS version R0070
- Addition of document for China RoHS compliance and California Perchlorate, Lithium battery disposal warning and EFUP20 sticker for China RoHS compliance
- Addition of trusted platform management module
- FBDIMM 1.5V voltage regulator resistor tolerance changes for improved VR circuitry robustness at locations R5C23 & R5C26

## Customer Impact of Change and Recommended Action:

Intel recommends limited customer validation for these changes

## Products Affected / Intel Ordering Codes:

Pre Change Product Code	Pre Change MM#	Pre Change TA	Pre Change PBA	Post Change MM#	Post Change TA	Post Change PBA
S5000XVNSATA 886040	886040	D50362-003	D37261-803	886040	D50362-004	D37261-901
S5000XVNSAS 886042	886042	D50361-003	D25028-803	886042	D50361-004	D25028-804
BB5000XVNSATA 886321	886321	D49140-803	D37261-803	888713	D49140-901	D37261-901
BB5000XVNSAS 886322	886322	D49141-803	D25028-803	888714	D49141-804	D25028-804

## Reference Documents / Attachments:

**Document:** **Location #:**

## PCN Revision History:

<b>Date of Revision:</b>	<b>Revision Number:</b>	<b>Reason:</b>
December 20, 2006	00	Originally Published PCN
January 12, 2007	01	Adding FBDIMM resistor change