



Product Change Notification

108623 - 01

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 108623 - 01
Change Title: Intel® System Controller Hub (SCH) Chipset, PCN 108623-01, Product Design, update from stepping D-1 to D-2 to introduce feature enhancements, Reason for Revision: Removing MM# 897638 from Conversion
Date of Publication: August 25, 2009

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Aug 04, 2008
Date of Qualification Data Availability:	Aug 29, 2008
Date Customer Must be Ready to Receive Post-Conversion Material:	Nov 14, 2008
Date of First Availability of Post-Conversion Material:	Sep 01, 2008

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Reason for Revision: Removing MM# 897638 from Conversion

MM# 897638 has been removed and will not convert to the new D2 stepping.

FROM THE ORIGINAL PCN-00:

The Mobile Intel® System Controller Hub (SCH) Chipset for low power mobile platforms is being updated from stepping D-1 to D-2 to introduce feature enhancements. The feature enhancements are:

- Support for Trusted Platform Module (security & manageability)
- Support for USB Client Full-Speed mode (legacy device compatibility)
- Fix for USB "Port 0" issue
- Fix for "UAA Audio" issue

Please refer to the technical specification update for a complete list of changes on D-2 stepping

There is also a change to the chipset Rev ID.

Customer Impact of Change and Recommended Action:

The new System Controller Hub (SCH) Chipset version will be backwards compatible with platforms validated with the previous chipsets.

- Intel does not expect any system board changes as a direct result of converting from Intel® SCH D-1 to D-2 stepping
- Designs which have passed system WHQL with Intel® SCH D-1 will not need to go through system WHQL again with D-2
- Intel plans to provide Intel® SCH Qualification Samples (QS) to facilitate customer qualification of D-1 to D-2 stepping conversion
- Intel® SCH D-1 stepping carries a WHQL contingency for the "UAA Audio" issue. This contingency expires on November 30th 2008.

Intel will only ship Intel® SCH D-2 SKUs after the November 30th date.

Customers need to update their ordering process to place new system controller hub chipset orders with the new S-Spec and MM# once they become available for order.

Products Affected / Intel Ordering Codes:

Processor#	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion S-Spec	Post Conversion MM#
Intel® System Controller Hub	AF82UL11L	S LB4W	897636	QV25	899258	S LGFS	899762
Intel® System Controller Hub	AF82US15W	S LB4U	897637	QV23	899256	S LGFQ	899760

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

July 28, 2008

August 25, 2009

Revision Number:

00

01

Reason:

Originally Published PCN

Removing MM# 897638 from Conversion