



Product Change Notification

108547 - 01

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

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Product Change Notification

Change Notification #: 108547 - 01
Change Title: Intel(R) IP Network Server NSC2U, PCN 108547-01, Product Design, Revised IDE cable fold and Boot Bank Selection Jumper standard shunt, Reason for Revision: Update TA revision numbers
Date of Publication: January 12, 2009

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

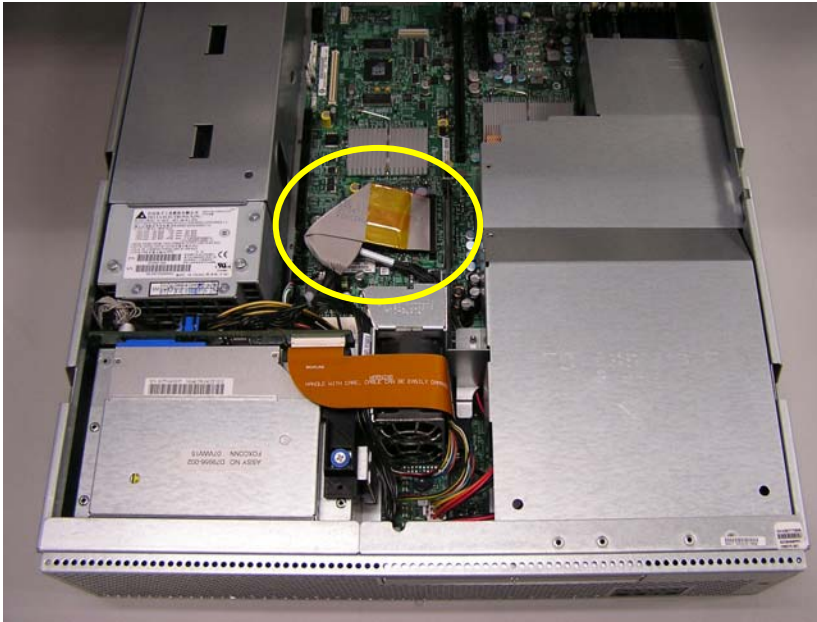
Date Customer Must be Ready to Receive Post-Conversion Material:	Sep 19, 2008
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Description of Change to the Customer:

Reason for Revision: Update TA revision numbers

Intel has revised the fold of the IDE cable and removed the tab from the BIOS Bank Select Jumper (BBSJ) to minimize cable intrusion into the PCI keep-out zone.

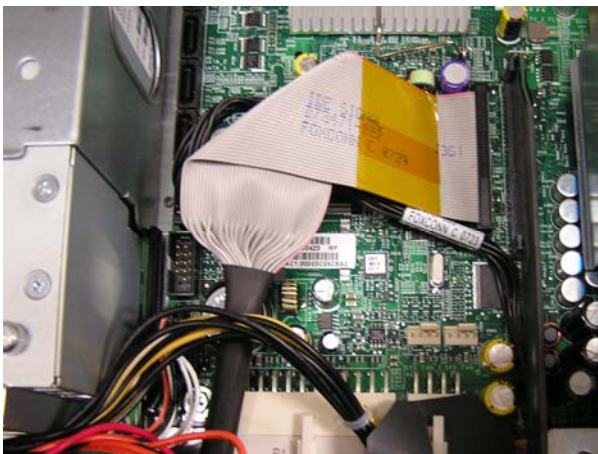
The IDE cable length and design remain the same. The change to the IDE cable is limited to a revised fold. The revised "tab-less" jumper is a standard shunt.



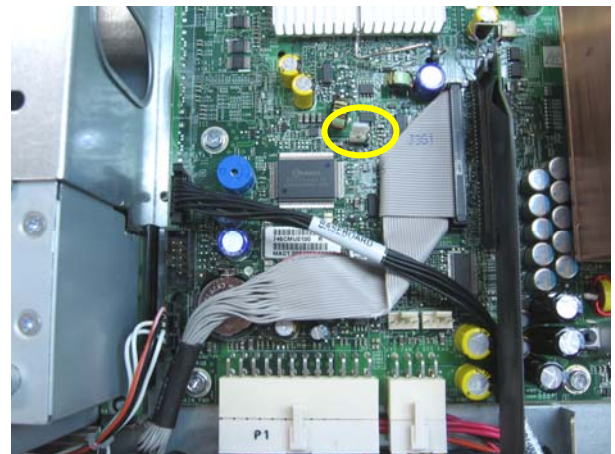
Location of IDE cable and BBSJ on a NSC2U server with the top cover removed.

The photos on the left show the original IDE cable fold with the cable routed over the top of the BBSJ and the USB cable.

The photos on the right show the revised fold and routing. The result of the revised cable routing and fold the IDE cable rests closer to the system board. The BBSJ is exposed and highlighted by a yellow circle.



Original cable fold



Revised cable fold

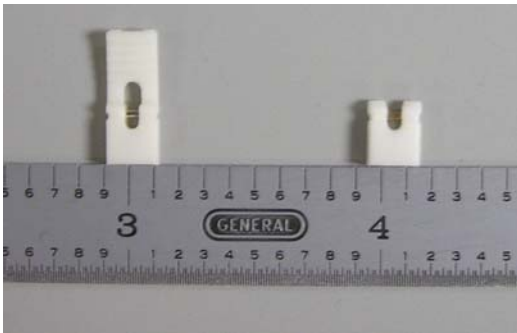


Close up of original IDE fold & BBSJ



Close up of revised IDE cable fold & original BBSJ

The tab on the original shunt is designed to improve shunt access in the event the position must be changed. The tab-less shunt is a standard shunt used in electronic equipment. The BBSJ is accessed only in the event of a corrupted or failed BIOS update process. Corruption of a BIOS bank is a rare occurrence hence the BBSJ is seldom used.



Shunt with tab on left;
Shunt without tab on the right

Customer Impact of Change and Recommended Action:

Intel anticipates no customer impact based on this change.

Products Affected / Intel Ordering Codes:

Pre Change Product Code	Pre Change MM#	Pre Change TA	Post Change TA
NSCD0201W 889357	889357	D86176-003	D86176-004
NSCA0201W 889358	889358	D86178-003	D86178-004

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
June 18, 2008	00	Originally Published PCN
January 12, 2009	01	Update TA revision numbers