



Product Change Notification

109442 - 00

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jcb.ijkk@intel.com

Copyright © Intel Corporation 2009. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



Product Change Notification

Change Notification #: 109442 - 00
Change Title: Intel(R) Compute Module MFS5000SI, Intel(R) Compute Module MFS5520VI, PCN 109442-00, Product Material, Reduce the number, size and location of tooling holes on the compute module top covers.
Date of Publication: June 22, 2009

Key Characteristics of the Change:

Product Material

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Jul 24, 2009
---	--------------

Description of Change to the Customer:

Reducing the number of tooling holes on the compute module top covers from four (4) to three (3). Changing the hole diameter from 8.05 mm to 5.05 mm and reconfiguring the location of the holes away from the configuration label. Holes are used in manufacturing of the top cover and a new tool requires fewer and smaller holes during the cutting and bending process. In addition, the holes are being moved so they are not covered by the configuration label allowing dust to stick to label adhesive showing through a hole.

Customer Impact of Change and Recommended Action:

This change will have no impact on system operation and no customer action is required.

Products Affected / Intel Ordering Codes:

Pre Change Product Code	Pre Change MM#	Pre Change TA	Post Change Product Code	Post Change MM#	Post Change TA
MFS5000SI 900409	900409	D91952-00X	MFS5000SI	900409	D91952-00X
MFS5000SIB 900410	900410	E19099-00X	MFS5000SIB	900410	E19099-00X
MFS5520VI 901369	901369	E42643-00X	MFS5520VI	901369	E42643-00X
MFS5520VIB 901699	901699	E54314-00X	MFS5520VIB	901699	E54314-00X

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

June 22, 2009

00

Originally Published PCN