



Product Change Notification

109482 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 109482 - 00
Change Title: Boxed Intel® Pentium® Processor E2210, PCN 109482-00, Product Design, M Step to R Step Conversion for Boxed
Date of Publication: July 16, 2009

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	May 01, 2009
Date of First Availability of Post-Conversion Material:	May 01, 2009

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel is initiating an M-0 to R-0 stepping conversion for the Intel® E2210 processor where it will undergo the following changes:

- CPUID Change
 - CPUID = 0x1067A (vs CPUID on M step which was 10676h)
- Power Status Indicator (PSI) support
- PEFI Implementation change
- ACNT2
 - Improved mechanism for determining processor utilization. To be used for more efficient P-state determination.
 - Use of ACNT2 reporting mechanism is enabled by setting the MSR IA32_MISC_ENABLES[27] address 0x1A0.
 - Availability of this feature is reported via CPUID (EAX = 6) returned in ECX bit 1
- New instructions added - XSAVE/XRSTOR
 - New ISA extension for save/restoring context of x87, SSE, and future processor state.
- Package change to halide free package

Customer Impact of Change and Recommended Action:

It is recommended that customers perform their standard validation testing on the new stepping. Customers should be ready to receive a combination of both M-0 stepping material and R-0 by the "Date Customer Must be Ready to Receive Post-Conversion Material" above.

Products Affected / Intel Ordering Codes:

Boxed Product List										
Marketing Name	Processor #	Frequency	Pre Conversion				Post Conversion			
			Stepping	Product Code	S-Spec	MM#	Product Code	S-Spec	MM#	Stepping
Boxed Intel® Pentium® Processor	E2210	2.20 GHz	M-0	BXC80571E2210	S LB7N	903205	BXC80571E2210	S LB9R	903204	R-0

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

July 16, 2009

Revision Number:

00

Reason:

Originally Published PCN