



Product Change Notification

107791 - 00

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jcb.ijkk@intel.com

Copyright © Intel Corporation 2007. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



Product Change Notification

Change Notification #: 107791 - 00
Change Title: Intel IP Network Server NSW1U, PCN 107791-00, Product Design, Software Update, Label Change, update PCIe riser
Date of Publication: August 07, 2007

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Aug 31, 2007
---	--------------

Description of Change to the Customer:

1. Change Software Update Package (SUP) to SUP17 to enable support for G Stepping of 64-bit Dual-Core Intel Xeon 5100 series processors.

- Change BIOS version 71 to BIOS version 79
- Change Baseboard Management Controller (BMC) from version HB_BMC15 to HB_BMC16
- Change FRU from FHB10 to FHB12.
- Please refer to release notes available online (link provided below) for SUP17 for additional changes included in this update.

2. Update to add Argentina IRAM certification to product Label, as shown below.



3. Change gold plating thickness on PCIe Riser board connector from 15u” to board with 30u” of Gold to improve long term reliability.

Customer Impact of Change and Recommended Action:

1. Customers should perform validation. The SUP17 release notes and latest associated drivers can be found here: <http://www.intel.com/design/servers/cg/tigw1u/tools.htm>
2. No impact.
3. No impact.

Products Affected / Intel Ordering Codes

Pre Change Product Code	Pre Change MM#	Pre Change TA	Post Change TA
NSWA0201W	885432	D74310-005	D74310-006
NSWD0201W	885433	D74311-006	D74311-007
NSWA0301W	885501	D74313-005	D74313-006
NSWA0401W	888296	D77412-005	D77412-006

Reference Documents / Attachments:

Document: _____ Location #: _____

PCN Revision History:

Date of Revision: August 7, 2007 Revision Number: 00 Reason: Originally Published PCN