



Product Change Notification

Change Notification #: 119655-00

Change Title: Select Intel Agilex® 7 F & I series Devices,
PCN 119655-00, Documentation,
DDR4 LRDIMM 1DPC 4R Fmax Update

Date of Publication: August 25, 2023

Key Characteristics of the Change:

Documentation

Forecasted Key Milestones:

Availability of Update	Now
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Description of Change to the Customer:

This is the same change described in ADV2316 issued on August 21, 2023.

Intel is notifying customers of an important update to the Intel Agilex® 7 F & I series DDR4 LRDIMM 1DPC 4R Fmax.

Table 1

Change From (existing)	Change To
Agilex DDR4 LRDIMM 1DPC 4R Fmax at 1333MHz for -1 & -2 speed grades, and 1200MHz for -3 speed grade.	Agilex DDR4 LRDIMM 1DPC 4R Fmax for all speed grades limited to 1067MHz

Table 2

Updated Documents	Document links
Intel Agilex® 7 F/I Series External Memory Interface (EMIF) Spec Estimator already reflecting the	https://www.intel.com/content/www/us/en/support/programmable/support-resources/support-centers/emif-spec-estimator.html
External Memory Interfaces Intel Agilex® 7 F-Series and I-Series FPGA IP Core Release Notes (see Table 2, LRDIMM: 1 DPC 4R row)	https://www.intel.com/content/www/us/en/docs/programmable/683334/23-2-2-7-1/external-memory-interfaces-f-series-19512.html

Customer Impact of Change and Recommended Action:

Customers are requested to review the changes and determine the impact to their designs.

For questions or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected/Intel Ordering Codes:

All Intel Agilex 7 F & I series devices with -1, -2, -3 speed grades. The list of affected part numbers (OPNs) can be downloaded in Excel form: <https://cdrdv2.intel.com/v1/dl/getContent/786755>

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
August 25, 2023	00	Originally Published PCN



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No response from customers will be deemed an acceptance of the change, and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below.

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