



Product Change Notification

105118 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 105118 - 00
Change Title: CPI200B2, CPI400B2, CPI400B2JP, PCN 105118-00, FYI, Design Change to eliminate the possibility of a channel incorrectly sensing that a ring is present, causing the board to stay in an off-hook state following a fax.
Date of Publication: May 19, 2005

Type of Change Notification:
FYI - (For Your Information)

Key Characteristics of the Change:
Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	May 27, 2005
Date of First Availability of Post-Conversion Material:	May 27, 2005

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel has verified and rectified an issue experienced with a small percentage of deployed CPI400B2 which resulted in a stop-ship in April 2005. This issue, which occurs when a channel incorrectly senses that a ring is present, causes the board to stay in an off-hook state following a fax. Although no deployed CPI200B2 boards have been reported to have experienced the issue, the product is included in this PCN considering it is in the same product family.

The specific change is to the values of resistor and capacitor network to increase the margin of Ring Detection Circuit.

Customer Impact of Change and Recommended Action:

To date, only ten (10) CPI400B2 boards (out of 1000+ distributed) have experienced this issue.

Intel has replicated the issue, optimized component values & tolerances, and verified the optimizations through rigorous temperature & design verification testing to preclude the

potential reoccurrence of the issue. Deployed field configurations which have not exhibited the described condition will in all likelihood not experience this issue.

Intel recommends that these boards be returned to Intel for rework. You may return the boards right away, or if the board is deployed and currently functioning properly, you may elect to wait to see if the board exhibits the described symptoms. All returned boards will be repaired free of charge via Intel's Direct Return Authorization (DRA) process. The boards must be returned within the applicable warranty period to receive the free of charge repair.

To return a board for a repair:

US Distributors: Please fill out the online DRA form, mentioning "CPIB2 Resistor Issue" in the problem description field, at:

<http://www.intel.com/support/motherboards/draform.htm>

Non-US Distributors: Please contact your RMA Call Center to arrange for return and repair.

To avoid shipping expenses, please request the "Intel FedEx account number" when completing the DRA (include this in the problem description section), or when speaking to a representative in the RMA Call Center.

Effective immediately, all products returned to Intel for repair will be modified free of charge to correct this situation.

Intel's standard policy to rework boards is 5 business days from the time Intel receives the boards until the time Intel ships the boards back to the customer. If you require a shorter return time, please indicate this on the DRA form under the "Comments" field.

Intel requires that customers who have resold any of the affected board(s) listed provide this Product Change Notification (or the details contained herein) to the purchaser of such boards.

See part number details below.

Products Affected / Intel Ordering Codes:

Products Table

Affected Product Code	Affected MM#	Pre change TA#	Post-Change TA#
CPI400B2	853752*	C83920-002	C83920-003
CPI400B2	844995	99-7098-002	n/a
CPI400B2JP	845901*	C97535-001	C97535-002
CPI200B2	853741*	C84023-002	C84023-003
CPI200B2	844996	99-7097-002	n/a

Active products are denoted by *

Reference Documents / Attachments:

Document:

Location #:

DRA form

<http://www.intel.com/support/motherboards/draform.htm>

PCN Revision History:

Date of Revision:

May 19, 2005

Revision Number:

00

Reason:

Originally Published PCN