



Product Change Notification

Change Notification #: 115514 - 00
Change Title: Intel® NUC Kit, NUC7i3BNH,
Intel® NUC Kit, NUC7i3BNK,
Intel® NUC Board NUC7i3BNB,
PCN 115514-00, Product Design,
BIOS Update
Date of Publication: May 15, 2017

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	May 29, 2017
---	--------------

Description of Change to the Customer:

The Intel® NUC SKUs listed in the products affected table below will have the following change.

- The BIOS will be updated from BNKBL357.86A.0042 to BNKBL357.86A.0045 for Optane support. A complete list of the BIOS changes can be found in the BIOS release notes. The BIOS updates and release notes can be found at <http://downloadcenter.intel.com>.

Customer Impact of Change and Recommended Action:

It is recommended that customers evaluate this new BIOS to ensure a smooth transition. This change has been thoroughly evaluated to ensure that there are no quality or reliability implications to our customers. Intel validation of a new board revision does not include testing with older versions of BIOS.

Milestone dates are estimates and subject to change based on business and operational conditions.

Customer can expect to receive mixed inventory until current inventory levels are depleted.

Please contact your local Intel Field Sales Rep if you have any further questions about these changes.

Products Affected / Intel Ordering Codes:

Product Code	MM#	Pre Change SA	Pre Change AA	Post Change SA	Post Change AA
BOXNUC7I3BNK	950963	J31188-305	J22859-305	J31188-306	J22859-306
BOXNUC7I3BNKL	950965	J50876-305	J22859-305	J50876-306	J22859-306
BOXNUC7I3BNH	950967	J31191-305	J22859-305	J31191-306	J22859-306
BOXNUC7I3BNHL	950969	J50874-305	J22859-305	J50874-306	J22859-306
BLKNUC7I3BNB	955782	NA	J22859-305	NA	J22859-306

PCN Revision History:**Date of Revision:**

May 15, 2017

Revision Number:

00

Reason:

Originally Published PCN



Product Change Notification

115514 - 00

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

A "Mission Critical Application" is any application in which failure of the Intel Product could result, directly or indirectly, in personal injury or death. SHOULD YOU PURCHASE OR USE INTEL'S PRODUCTS FOR ANY SUCH MISSION CRITICAL APPLICATION, YOU SHALL INDEMNIFY AND HOLD INTEL AND ITS SUBSIDIARIES, SUBCONTRACTORS AND AFFILIATES, AND THE DIRECTORS, OFFICERS, AND EMPLOYEES OF EACH, HARMLESS AGAINST ALL CLAIMS COSTS, DAMAGES, AND EXPENSES AND REASONABLE ATTORNEYS' FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PRODUCT LIABILITY, PERSONAL INJURY, OR DEATH ARISING IN ANY WAY OUT OF SUCH MISSION CRITICAL APPLICATION, WHETHER OR NOT INTEL OR ITS SUBCONTRACTOR WAS NEGLIGENT IN THE DESIGN, MANUFACTURE, OR WARNING OF THE INTEL PRODUCT OR ANY OF ITS PARTS.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmopcn@intel.com

Asia Pacific/PRC Contact: apagccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

Copyright © Intel Corporation 2017. Other names and brands may be claimed as the property of others.

3D XPoint, Axxia, Basis, Basis Peak, BlueMoon, BunnyPeople, Celeron, Centrino, Cilk, Curie, Flexpipe, Intel, the Intel logo, Intel Atom, Intel CoFluent, Intel Core, Intel. Experience What's Inside, the Intel. Experience What's Inside logo, Intel Inside, the Intel Inside logo, Intel Insider, Intel Joule, Intel RealSense, Intel SingleDriver, Intel SpeedStep, Intel Unite, Intel vPro, Intel Xeon Phi, Intel XScale, InTru, the InTru logo, the InTru Inside logo, InTru soundmark, Iris, Itanium, MCS, MMX, Optane, Ostro, Pali, Pentium, picoArray, Picochip, picoXcell, Puma, Quark, SMARTi, Soletta, Sound Mark, StarPro, Stay With It, the Engineering Stay With It logo, StreamSight, Tarari, The Journey Inside, Thunderbolt, the Thunderbolt logo, Transcede, True Key, Ultrabook, VTune, Xeon, X-GOLD, XMM, X-PMU and XPOSYS are trademarks of Intel Corporation or its subsidiaries in the U.S. and/or other countries.

Microsoft, Windows, and the Windows logo are trademarks, or registered trademarks of Microsoft Corporation in the United States and/or other countries. Java is a registered trademark of Oracle and/or its affiliates. Bluetooth is a trademark owned by its proprietor and used by Intel Corporation under license. Intel Corporation uses the Palm OS* Ready mark under license from Palm, Inc. OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission by Khronos. Learn how to use Intel Trademarks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.