



Product Change Notification

106451 - 00

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

Copyright © Intel Corporation 2006. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



Product Change Notification

Change Notification #: 106451 - 00
Change Title: Intel NetStructure® DM/V-A, DM/V-B Custom Products, PCN 106451-00, Product Material, Product migration to comply with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) Directive
Date of Publication: June 25, 2006

Key Characteristics of the Change:

Product Material

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Aug 07, 2006
Date of First Availability of Post-Conversion Material:	Aug 07, 2006

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel NetStructure® DMV300AE1PCI, DMV240AT1PNLK, DMV300BTEP and DMV300BTEPNLK Custom products will be undergoing changes to several of components. These changes are to remove or reduce a number of restricted substances below the levels required by the European Union's Directive on the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS Directive), 2002/95/EC, as amended. The RoHS Directive limits the amount of lead (Pb), mercury (Hg), cadmium (Cd), hexavalent chromium (Cr VI), polybrominated biphenyls (PBB), or polybrominated diphenyl ethers (PBDE) in electrical and electronic products.

Customer Impact of Change and Recommended Action:

The customer should see no change to form, fit, or function as a result of the changes. As part of this process, Intel will re-validate all affected product parameters to ensure that each affected product performs like the product it replaces. There are no software (system release) changes required for the replacement products listed in the product table below.

Product Codes and Markings:

The replacement products will be marked such that one will be able to tell from reading the box labels or the product labels whether each product is designed to comply with the RoHS Directive and whether the product relies on a leaded solder exemption under the RoHS Directive. As you can tell from the product code list below, products relying on a leaded solder exemption (commonly known as RoHS 5/6) are

appended with the letter “W”. The Product Code (Item Market Name) is both on the shipping box and on the board front panel bracket.

If customers should require a document from Intel that states the level of RoHS compliance for these products, a Material Declaration Datasheet (MDDS) is available for each replacement product and can be provided if requested.

See the table below for new Product Codes and MM numbers. The replacement products are now orderable. The pre change products will still be available to order for approximately one month after RoHS 5/6 compliant product are available. The First Availability Date of RoHS Product is an 80 percent confidence date. If this date slips the availability of pre change product will be extended for an equal period of time.

Contact your sales representative for additional migration information and/or any issues you may have with this plan.

Products Affected / Intel Ordering Codes:

Board Products Table

Pre Change Product Code	Pre Change MM#	Post-Change Product Code	Post-Change MM#
DMV300AE1PCI	857709	DMV300AE1PCIW	881823
DMV240AT1PNLK	860746	DMV240AT1PWNLK	881799
DMV300BTEP	873938	DMV300BTEPW	881800
DMV300BTEPNLK	873939	DMV300BTEPWNLK	881801

Reference Documents / Attachments:

Document:

Intel RoHS / Lead (Pb) Free Site

Location #:

<http://www.intel.com/technology/silicon/leadfree.htm>

Intel RoHS / Lead (Pb) Compliant Product Search

<http://indigo.intel.com/leadfree/Public/LeadfreeProductSearch.aspx>

PCN Revision History:

Date of Revision:

June 25, 2006

Revision Number:

00

Reason:

Originally Published PCN