



Product Change Notification

103946 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geography location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 103946 - 00
Change Title: Intel®TXN1322xx Transponder, PCN 103946-00, FFF
Date of Publication: March 19, 2004

Type of Change Notification:
FFF - (Form-Fit-Function)

Key Characteristics of the Change:
Product Design
Order Code

Forecasted Key Milestones:

Date of Samples Availability:	Mar 08, 2004
Date of Qualification Data Availability:	Mar 08, 2004
Date Customer Must be Ready to Receive Post-Conversion Material:	Jun 30, 2004
Date of First Availability of Post-Conversion Material:	Mar 08, 2004

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

In the course of Intel's continued validation testing, an issue was found with the LsEnable timing. Intel's TXN1322xx transponder did not meet the 300pin MSA LsEnable timing specification of 10ms at lower temperatures. In order for Intel to meet the requirements, the following changes were made to the transponder to speed up the LsEnable timing:

1. Resistor change to speed up the LsEnable timing and an OP-amp change to ensure that there would not be an optical power blip during specific power down scenarios.
2. Change to the firmware to eliminate the polling delay from the micro-controller to initiate the LsEnable signal. In hardware operation mode, now only the LsEnable signal from the 300pin connector initiates the laser turn on.
3. Replacement of temperature dependent capacitor with a X5R dielectric capacitor to reduce variation of LsEnable timing over temperature

Customer Impact of Change and Recommended Action:

There is an impact to the form fit or function of the optical transceiver with the changes discussed above based on the LsEnable timing change. These modifications made to the TXN1322xx do not require any line card modifications. The changes were made to decrease the LsEnable timing of the "Production" version of the transponder to meet the 300pin MSA specification of 10ms.

Intel has done 4 corner testing, non-operational testing, shock and vibration testing and operational testing on transponder. The results are available in a presentation that is available to customers.

There is a change to Material master ID (MM #) based on the transition to the "LsEnable Fix" version of the transponder. Please use the chart below for the updated MM# that will be used to order future shipments.

Product Change Summary:

Intel continually strives to improve the products that it sells to customers. As part of this process Intel tries to meet the requirements of its customers and the industry specifications under all conditions. Intel determined that there was an issue with meeting LsEnable timing (10ms) at lower temperatures. A solution was found and implemented. The LsEnable changes affect the function of the transponder and Intel has done extensive testing to address all the issues regarding reliability and functionality of the transponders. Validation testing has been completed and is available in a presentation that you can request from you local field sales team.

Products Affected / Intel Ordering Codes:

System Products Table

Affected Product Code	Pre-Change MM#	Post Change Part Code	Post-Change MM#
TXN132274015A01	859119	No change	861752
TXN132271013A03	858985	No change	861753
TXN132201013D01J	858890	TXN132201013DJ1	861754
TXN132204015D01J	858407	TXN132204015DJ1	861755
TXN132201013A01	858897	No Change	861823
TXN132201013Z02	859207	No Change	861807

Reference Documents / Attachments:

Document:	Location #:
LsEnable Solution Overview and Validation Results (ppt)	http://fdbl.intel.com or through the local field sales support team

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
March 19, 2004	00	Originally Published PCN