



# Product Change Notification

## 108271 - 01

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

**Europe Email:** [eccb@intel.com](mailto:eccb@intel.com)

**Japan Email:** [jccb.ijkk@intel.com](mailto:jccb.ijkk@intel.com)

Copyright © Intel Corporation 2008. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



# Product Change Notification

**Change Notification #:** 108271 - 01  
**Change Title:** Intel® IP Network Server NSC2U, PCN 108271-01, Product Design, Fab spin to incorporate design changes to the front panel I/O board, Reason for Revision: Update TA#s and implementation date  
**Date of Publication:** July 31, 2008

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Jun 16, 2008
---	--------------

## Description of Change to the Customer:

### Reason for Revision: Update TA#s and implementation date

This fab spin incorporates the following improvements made to the design of the front panel I/O board.

- 1) Added two surface mount resistors to stabilize the signal of the USB over-current pin on the unimplemented USB lane. This change integrates the blue wire fix referenced in Intel Technical Advisory TA-0881-1.
- 2) Removed unused flash sockets and replaced I2C multiplexer with a 0 Ohm resistor to improve reliability and manufacturability.

## Customer Impact of Change and Recommended Action:

These changes will be validated by Intel. Intel anticipates no impact to customers.

## Products Affected / Intel Ordering Codes:

Product Code	MM#	Pre Change TA	Post Change TA
NSCD0201W	889357	D86178-011	D86178-011
NSCA0201W	889358	D86176-011	D86176-011
NSCFPIOBDLPW	889812	D89056-006	D89056-006

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

Revision Number:

Reason:

February 7, 2008

00

Originally Published PCN

July 31, 2008

01

Update TA#s and implementation date