



Product Change Notification

108254 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 108254 - 00
Change Title: Boxed Dual-Core Intel® Xeon® processor LV 5148 (Woodcrest), PCN 108254-00, Product Design, B2 to G0 Stepping Change
Date of Publication: January 28, 2008

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Feb 29, 2008
Date of First Availability of Post-Conversion Material:	Feb 29, 2008

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

The Boxed Dual-Core Intel® Xeon® processor LV 5148 (Woodcrest) will convert from B-2 to G-0. Woodcrest will undergo the following changes:

Increased Energy Efficiency

- This SKU will receive some amount of C1E power reduction on G-step
- This SKU, which is already at the lowest bus to core frequency ratio or lowest voltage operating point, cannot drop to the lower power state required for Extended HALT

Enhanced Virtualization Technology

- APIC Task Priority Register (TPR) Virtualization
- NMI-window exiting
- Advanced VM-exit information for INS & OUTS

Woodcrest CUID will change from 06F6 to 06FB.

New SSPEC and MM numbers for the converting products

Customer Impact of Change and Recommended Action:

Intel is finalizing a technical solution to allow mixed stepping configurations of B-step and G-step Woodcrest on Bensley platforms. This solution requires appropriate microcode updates to be loaded; current microcode updates for Woodcrest B-2 will need to be replaced. Solution requires BIOS change; BIOS will detect mixed stepping configuration and set a bit in a Model Specific Register (MSR).

Customers creating their own BIOS should adhere to the guidelines provided in the "Dual-Core Intel(R) Xeon(R) Processor 5100 Series [Woodcrest] Mixed Steppings Support Guide, May 2007" that can be found on IBL (Intel Business Link)

- IBL Path: IBL Home > Information Desk > Processors > Server/Workstation > Dual Core Intel® Xeon® Processor 5100 Series > Technical

Once customers implement the BIOS update, they will be able to support same stepping and mixed stepping configurations. Customers should be ready to receive a combination of both B-2 stepping material and G-0 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" of February 29, 2008.

Products Affected / Intel Ordering Codes:

Boxed Dual-Core Intel® Xeon® processor LV 5148 (Woodcrest)									
Processor#	Frequency	Pre Stepping	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Stepping	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
5148	2.33 GHZ	B-2	BX805565148A	S LABH	890798	G-0	BX805565148A	S LAG4	892145
5148	2.33 GHZ	B-2	BX805565148P	S LABH	890799	G-0	BX805565148P	S LAG4	892146

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

January 28, 2008

Revision Number:

00

Reason:

Originally Published PCN