



Product Change Notification

104716 - 01

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 104716 - 01
Change Title: SSH4, PCN 104716-01, FFF, CLASS 1 Board:
Implement BIOS P15, BMC 2.8 and FRUSDR
5.6.A
Date of Publication: March 31, 2005

Type of Change Notification:
FFF - (Form-Fit-Function)

Key Characteristics of the Change:
Software

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Jan 28, 2005
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Description of Change to the Customer:

BIOS P15 includes microcode updates for Intel(R) Xeon(tm) Processor MP(tm) for processors with CPUIDs 0F11h, 0F22h, 0F25h, and 0F26h. BMC 2.8 and FRUSDR 5.6.A are current versions.

Customer Impact of Change and Recommended Action:

Evaluate when received

Products Affected / Intel Ordering Codes:

Board Products Table

Affected Product Code	Pre-Change MM#	Post-Change MM#	Pre-Change TA or AA	Post-Change TA or AA	Pre-Change PBA	Post-Change PBA
BSHBASE 858298	858298	869594	A75318-706	A75318-707	A60891-706	A60891-707

Reference Documents / Attachments:

Document: **Location #:**

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
January 6, 2005	00	Originally Published PCN
March 31, 2005	01	Correcting the Post Change MM number