



Product Change Notification

110554 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 110554 - 00
Change Title: Intel ® Compute Module MFS5520VIR, PCN 110554-00, Product Design, Product Material, Updating the Compute Module Chipset and Supporting BIOS and BMC.
Date of Publication: March 24, 2011

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	May 27, 2011
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Description of Change to the Customer:

Intel will be updating the Intel® Compute Module MFS5520VIR from the B3 stepping of the Intel® 5520 I/O Hub to the C2 stepping. In addition, the BIOS will be updated to S5500.86B.01.00.0054 and the BMC to 1.20.1 in support of the new chipset stepping.

Customer Impact of Change and Recommended Action:

Intel anticipates no impact to the customer and therefore no action is required.

Products Affected / Intel Ordering Codes:

Pre Change Product Code	Pre Change MM#	Pre Change TA	Pre Change PBA	Post Change Product Code	Post Change MM#	Post Change TA	Post Change PBA
MFS5520VIR	905716	E87653-003	E41515-406	MFS5520VIR	905716	E87653-004	E41515-452
MFS5520VIBR	905717	E87663-003	E41515-406	MFS5520VIBR	905717	E87663-004	E41515-452

PCN Revision History:

Date of Revision: March 24, 2011	Revision Number: 00	Reason: Originally Published PCN
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