



# Product Change Notification

**Change Notification #:** 119520-00

**Change Title:** Select Server System M50CYP2UR208,  
PCN 119520-00, Product Design,  
BIOS/BMC/ME/FRUSDR Firmware Update in  
Factory

**Date of Publication:** March 30, 2023

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

|   |                |
|---|----------------|
| <b>Date Customer Must be Ready to Receive Post-Conversion Material*</b> | April 14, 2023 |
|---|----------------|

\* Intel appreciates the customer's desire to receive the latest revision of products. However, Intel manages inventory on a first in first out (FIFO) basis at the MM# level. Subsequently, customer requests for a specific revision of material (below the MM#) will not be supported by Intel.

## Description of Change to the Customer:

Intel is implementing the following changes to the Intel® Server Board listed in the “Products Affected” table below:

1. Update BIOS revision to R01.01.0005
2. Update Manageability Engine (ME) Firmware revision to 04.04.04.062
3. Update Baseboard Management Controller (BMC) revision to 2.88.e5f45b9c
4. Update CPLD Firmware revision to 3.5
5. Update the FRUSDR revision to 0.40

## Customer Impact of Change and Recommended Action:

Intel anticipates no impact to customers. Please contact your local Intel Field Sales Representative if you have any further questions about these changes.

## Products Affected/Intel Ordering Codes:

| Marketing Name                            | Product Code | MM#    | Pre Change TA | Pre Change PBA | Post Change TA | Post Change PBA |
|---|--------------|--------|---------------|----------------|----------------|-----------------|
| Intel® Server System M50CYP2UR208, Single | M50CYP2UR208 | 99A3TT | M56269-001    | M57661-353     | M56269-002     | M57661-354      |

## PCN Revision History:

**Date of Revision:**

March 30, 2023

**Revision Number:**

00

**Reason:**

Originally Published PCN



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No response from customers will be deemed an acceptance of the change, and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below.

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific/PRC Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

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