



Product Change Notification

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 113653-00
Change Title: Intel® Puma™ 6 SoC CE2612, CE2612M & Intel® Puma™ 6-MG CE2652, CE2652M, CE2656, CE2656M, PCN 113653-00, Product Discontinuance, B-Step to D-Step Conversion
Date of Publication: February 24, 2015

Key Characteristics of the Change:

Product Discontinuance

Key Milestones:

Last Product Discontinuance Order Date:	August 28, 2015
Orders are Non-Cancelable and Non-Returnable After:	August 28, 2015
Last Product Discontinuance Shipment Date:	February 28, 2016

Description of Change to the Customer:

Intel is initiating a B-Step to D-Step conversion for the Intel® Puma™ 6 SoC & 6-MG where it will undergo the following changes:

- Adds support for DOCSIS 3.0 32x8.
- Adds support for Puma™ 6E (DOCSIS-less) designs.
- Architectural changes to add Nagra and NDS Conditional Access Support.
- Manufacturing improvements.

Customer Impact of Change and Recommended Action:

New S-Specs and MM numbers are required for the conversion.

A transition from B-Step to D-Step may require a change to the CEFDK or SDK, consult your Intel technical support team for further details.

The D-Step package is pin compatible with the B-Step package.

The D-Step has a functional change to the SLIC SPI interface that may have hardware/software implications *only if* a 4-wire interface was implemented in the design. A new compile-time option in R4.5 would mitigate any hardware changes. Consult your Intel technical support team for further details.

Customer needs to place new orders using the recommended replacement order code in the post-conversion (production) column within the table shown below.

Date of Samples Availability; effective now.

Orders are Non-Cancelable and Non-Returnable after August 28, 2015.

Last Product Discontinuance Shipment Date is February 28, 2016.

Products Affected / Intel Ordering Codes:

Chip Family	Part Number	Pre Conversion		Post-Conversion (Samples)		Post-Conversion (Production)	
		MM#	S-Spec	MM#	Q-Spec	MM#	S-Spec
Intel® Puma™ 6 SoCs	CE2612	930810	SR1HH	938953	QHAA	940446	SR275
	CE2612M	930978	SR1JP	936737	QGZF	940443	SR274
Intel® Puma™ 6MG SoCs	CE2652	930814	SR1HM	938969	QHAB	940484	SR278
	CE2652M	930815	SR1HN	939060	QHB9	940485	SR279
	CE2656	930811	SR1HJ	938975	QHAC	(1)	(1)
	CE2656M	930812	SR1HK	936733	QGZE	(1)	(1)

Notes:

1). Development work should begin on the Q-spec. The final production specification (S-spec) will typically be a custom SKU. Work with your local Intel representative to initiate building of the SKU.

2). Consult your local Intel representative if you are using a B-Step SKU NOT in the above table.

PCN Revision History:

Date of Revision:

Feb 24, 2015

Revision Number:

00

Reason:

Originally Published PCN