



Product Change Notification

103651 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geography location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 103651 - 00
Change Title: DMV160LP, DMV160LPEU,
DMV160LPHIZ, PCN 103651-00, FYI,
Correct Incompatibility with some Bios that
Results in Boards being Disabled at Startup
Date of Publication: November 18, 2003

Type of Change Notification:
FYI - (For Your Information)

Key Characteristics of the Change:
Product Design

Forecasted Key Milestones:

Date of First Availability of Post-Conversion Material:	Nov 18, 2003
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The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Product is not compliant with one aspect of the PCI Revision 2.2 specification. Some PCI based servers and PCs run a BIOS that uses a check-bit as part of the board recognition process. These DMV products do not comply with the specified use of the check-bit, resulting in the boards' disablement at startup. The ASIC where the check-bit resides has been redesigned to correct this error.

Customer Impact of Change and Recommended Action:

It is important to note that if there is any incompatibility with the BIOS, it will be found immediately at startup. The issue has only been observed with BIOS in the following servers from Compaq- ML370 and ML350.

This change has been thoroughly evaluated to ensure that it fixes the problem and does not have any additional negative functional implications to our customer.

At the customer's discretion, Intel will accept returns of existing product through the DRA process for re-work to incorporate these changes.

Customers should modify their databases in order to place orders against the new order codes. See details below.

Products Affected / Intel Ordering Codes:

Board Products Table

Affected Product Code	Pre-Change MM#	Post-Change MM#
DMV160LP	854431	857638
DMV160LPEU	851852	857627
DMV160LPHIZ	855397	857632

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

November 18, 2003

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Originally Published PCN