



# Product Change Notification

## # 102686 - 00

Information in this document is provided in connection with Intel® products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel® products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel® products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Copyright © Intel Corporation 2002. Third-party brands and names are the property of their respective owners.

Intel, Insight960, Intel386, Intel486, Intel740, IntelDX2, IntelDX4, IntelSX2, Intel GigaBlade, Intel StrataFlash, Intel Xeon™, Intel XScale, Itanium™, OverDrive, Pentium®, Pentium® II Xeon™, Pentium® III Xeon™, Solutions960, LANDesk, Celeron®, FlashFile, i386, i486, i960, iCAT, iCOMP, Intel SingleDriver technology, EtherExpress™, AnyPoint is a trademark or registered trademark of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.

**Change Notification #:** # 102686 - 00  
**Change Title:** Telecom,GCIXF6402BEC.B2,  
Stepping Conversion from Revision  
B1 to Revision B2.  
**Date of PCN Publication:** Thursday, July 18, 2002

**Type of Change Notification:**

Form-Fit-Function

**Key Characteristics of the Change:**

Product Design  
Documentation  
Product Marking  
Order Code

**Forecasted Key Milestones:**

<b>Date of Samples Availability:</b>	Jul 18, 2002
<b>Date of Qualification Data Availability:</b>	Sep 18, 2002
<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Oct 18, 2002
<b>Date of First Availability of Post-Conversion Material:</b>	Oct 18, 2002

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

**Description of Change to the Customer:**

Intel will be releasing revision B2 of the GCIXF6402BEC.B2 ATM Traffic controller device. Intel LXT6402 revision B2 fixes the multi VC and multi shaper issue described in the product specification update.

**Customer Impact of Change and Recommended Action:**

There are no change to the register set and any surrounding circuitry.

**Products Affected / Intel Ordering Codes:**

**Component Products Table**

<b>Pre-conversion Product Code</b>	<b>Pre- conversion Spec/ROM</b>	<b>Pre- conversion MM#</b>	<b>Post-conversion Product Code</b>	<b>Post- conversion Spec/ROM</b>	<b>Post- conversion MM#</b>
IXF6402BEC7.B1	Q E000	836706	GCIXF6402BEC.B2	QE000	849260

### **Reference Documents / Attachments:**

**Document:**

Qual Data

**Location #:**

<http://developer.intel.com>

### **PCN Revision History:**

**Date of Revision:**

Jul 18, 2002

**Revision Number:**

00

**Reason:**

Originally Published PCN