



Product Change Notification

107463 - 01

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 107463 - 01
Change Title: Intel® Core™2 Quad Processor Q6600 and Intel® Xeon® Processors X3220 and X3210, PCN 107463-01, Product Design, B-3 to G-0 Stepping Conversion, Reason for Revision: Update PCN description of change
Date of Publication: May 17, 2007

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	May 11, 2007
Date of Qualification Data Availability:	Jul 13, 2007
Date Customer Must be Ready to Receive Post-Conversion Material:	Jul 27, 2007
Date of First Availability of Post-Conversion Material:	Jul 16, 2007

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Reason for Revision: Update PCN description of change

The Intel® Core™2 Quad processor Q6600 and Intel® Xeon® processors X3220 and X3210 will undergo the following changes for the B-3 to G-0 processor stepping conversion:

- CUID will change from 06F7 to 06FB
- New S-specs for converting products
- The converting products will change from the 2005 Performance FMB(105W) to the 2005 Mainstream FMB (95W)
- The Electrical and Mechanical Specifications remain within the current specifications. Intel anticipates no changes to customer platforms designed to previous Intel guidelines.
- Thermal specification has changed: Maximum allowed Case Temperature for the Intel® Core™2 Quad processor Q6600 and Intel® Xeon® processors X3220 and X3210 on G-0 stepping has been increased by 11 °C. Tcontrol offset will remain same relative to 0 with increase in Maximum allowed Case Temperature, which will help reduce acoustics.

Customer Impact of Change and Recommended Action:

Minimal re-qualification and/or validation is expected for the G-0 stepping conversion due to no feature set changes between the B-3 and G-0 steppings. Thermal qualification may be required due to increase in Tcase. The Intel® Core™2 Quad desktop processors Q6600 and Intel® Xeon® processors X3220 and X3210 G-0 stepping will require a BIOS update.

- Refer to Intel® Core™2 Extreme Quad-Core Processor QX6000 Sequence and Intel® Core™2 Quad Processor Q6000 Sequence Electrical, Mechanical, and Thermal Specifications (EMTS)
- Refer to Quad-Core Intel® Xeon® Processor 3200 Series EMTS.

Products Affected / Intel Ordering Codes:

Processor Number	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qual Samples QDF#	Post Conversion Qual Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
Q6600	2.40 GHz	HH80562PH0568M	S L9UM	885492	QXVD	890380	HH80562PH0568M	SLACR	891049
X3220	2.40 GHz	HH80562QH0568M	S L9UP	885494	QYPU	890378	H80562QH0568M	SLACT	891050
X3210	2.13 GHz	HH80562QH0468M	S L9UQ	885495	QYPV	890381	H80562QH0468M	SLACU	891048

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

April 25, 2007

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Originally Published PCN

May 17, 2007

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Update PCN description of change