



Product Change Notification

105802 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 105802 - 00
Change Title: Tray pocket change, PCN 105802-00, FFF, Changing from sitting ledge to tapered for packages with 9x11/11x9 dimensions
Date of Publication: February 28, 2006

Type of Change Notification:
FFF - (Form-Fit-Function)

Key Characteristics of the Change:
Transport Media

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	March 31, 2006
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Description of Change to the Customer:

Intel has made a change to the design of the tray pocket for packages with x/y dimensions of 9x11/11x9. The tray pocket design will be changed to a tapered pocket from the current ledge sitting design. The new tapered pocket JEDEC tray continues to meet the current design standards for 9x11/11x9 Packages (Generic Matrix Tray for Shipping and Handling, JEDEC Standard No-95-1 Nov. 1997 Revision C) Existing tray inventory will be depleted and replaced by new tray.

A tapered-pocket design can prevent units from getting misplaced in the pockets that could lead to chips.

Customer Impact of Change and Recommended Action:

Intel Corporation does not anticipate any impact to customer use.

There will be no change to the 9x11/11x9 package sizes or materials and the product will continue to meet Intel's stringent quality requirements.

Dimensions on the new trays are identical on all key parameters; including device seating height, pocket pitch and device orientation in the tray. There is no anticipated impact to the customer pick and place equipment. Older trays will interchangeably stack with the new tray. There is no change to the tray material and supplier.

Change to the JEDEC (shipping media): All critical dimensions on the JEDEC tray meet the same form, fit and function as the current manufacturer. Device unit count will remain the same at 290 pockets per tray.

The new JEDEC tray design will be identified by a new part number as shown below. In addition, during the transition period, a pink stripe will be present on two sides of the new tray (tapered pocket design) to aid in visually identifying the new tray from the old. See example of placement of the stripe in the diagram below. These stripes will be discontinued once the transition is complete.

The transition is expected to be completed by May, 2006. Until then, the revised JEDEC trays will be shipped interchangeably with existing trays and tray types may be mixed within shipments.

JEDEC tray part revision added:

	Current Tray	New Tray
Seating ledge:	flat	tapered
P/N	A82221-003	500042516

Products Affected / Intel Ordering Codes:

Component Product Table

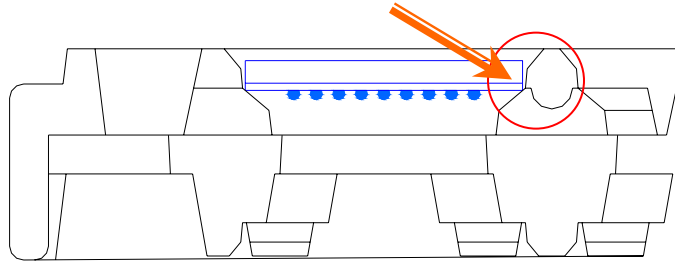
Affected Product Code	Affected Spec/ROM	Affected MM#
PF38F4060L0YBB0867108		867108
PF38F4060L0YBB1872937		872937
PF38F4060L0YTB0867359		867359
PF38F4060L0YTB1872900		872900
PF38F4460LLYBB0874123		874123
PF38F4460LVYBB0868471		868471
PF38F4460LVYBB1873321		873321
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PF38F4460LVYTB1873326		873326
PF38F4462LLYDB0861525		861525
PF38F4462LLYDB0S L7ZX S L7ZX		865776
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PF58F0016LVYBB0871507		871507
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PH28F256L18T85 875135		875135
PH28F256L30B85 875084		875084
PH28F256L30T85 875082		875082
RD38F4460LVYBB0867365		867365
RD38F4460LVYGB0867846		867846
RD38F4460LVYTB0867368		867368

Reference Documents / Attachments:

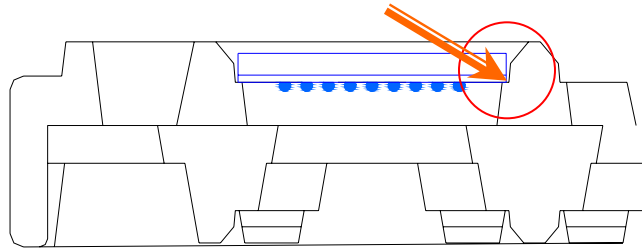
Document:

Location #:

[New design tapered pocket](#)

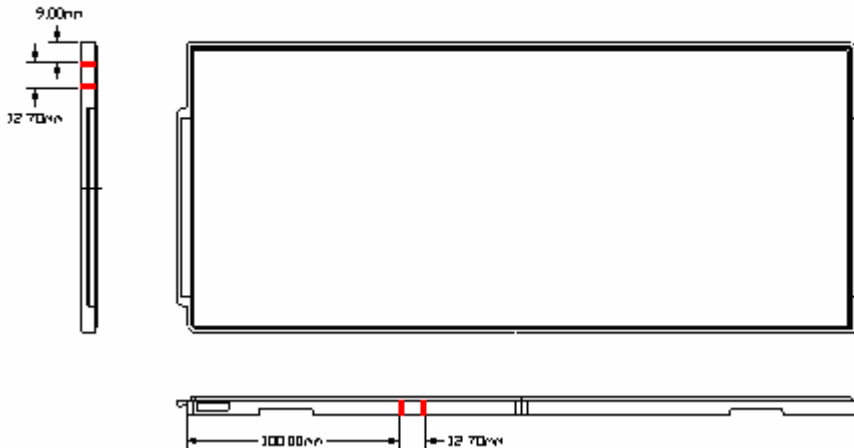


[Old design seating ledge pocket](#)



Intel Flash Striping

<Striping System>



PCN Revision History:

Date of Revision:

January 4, 2006

Revision Number:

00

Reason:

Originally Published PCN