



Product Change Notification

Change Notification #: 116027 - 00
Change Title: Intel® QuickAssist Adapter 8950-SCCP
Product Code IQA89501G1P5 MM#929848,
PCN 116027-00, Product Design, Label,
Serial Number addition and Regulatory
update
Date of Publication: January 17, 2018

Key Characteristics of the Change:

Product Design, Label

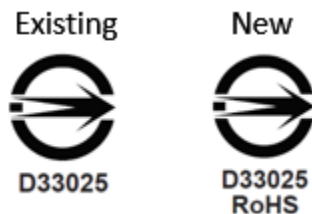
Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	February 21, 2018
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Description of Change to the Customer:

The Intel® QuickAssist Adapter 8950-SCCP IQA89501G1P5, MM#929848 will undergo the following changes:

1. An 11 digit Serial Number (SN) will be added to the contents of the EEPROM image. Customers have to use the PLX tool to read the Serial number in the PLX EEPROM:
Windows - PLX PEX Device Editor
Linux - PLX Command Line Monitor
Available at: <https://www.broadcom.com/products/pcie-switches-bridges/software-dev-kit>
2. The Taiwan BSMI has a new RoHS declaration and inspection mark. The updated BSMI logo will be reflected on the PCB via a cover up label.



3. As a result of this change, the PBA (printed board assembly) and TA (top assembly) numbers will roll as noted in the affected products table within this PCN notice. If the PBA/TA numbers are printed on any product or box labels, those will show the PBA/TA dash roll as well.

Customer Impact of Change and Recommended Action:

Customers can expect to receive mixed inventory until current inventory levels are depleted.

Please contact your local Intel Field Sales Rep if you have any further questions about these changes.

Products Affected / Intel Ordering Codes:

Product Code	MM#	Pre Change TA	Pre Change PBA	Post Change TA	Post Change PBA
IQA89501G1P5	929848	H16121-003	G98431-006	H16121-004	G98431-007

PCN Revision History:

Date of Revision:

January 17, 2018

Revision Number:

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Reason:

Originally Published PCN



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The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

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Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

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