



# Product Change Notification

## 106653 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

**Europe Email:** [eccb@intel.com](mailto:eccb@intel.com)

**Japan Email:** [jccb.ijkk@intel.com](mailto:jccb.ijkk@intel.com)

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# Product Change Notification

**Change Notification #:** 106653 - 00  
**Change Title:** SE7520JR2, PCN 106653-00, Product Design, Class 2: FAB Change to SE7520JR2ATAD1  
**Date of Publication:** August 04, 2006

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Aug 22, 2006
<b>Date of First Availability of Post-Conversion Material:</b>	Aug 22, 2006

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

A FAB change is being made to the Intel(R) Server Board SE7520JR2ATAD1. The changes are

- 1) Add pull up (4.7K ohm resistor) and pull down (10K ohm resistor) site to reset pin of Heccta (U5J1).
- 2) Change therm trip gate from AND gate to tri-state buffer (74LVC126A) and add 2 10K ohm pull up resistors.
- 3) Change R3F20 to stuff.
- 4) Change C2T6 from 0.1uF to 1.0uF for VTT VR change.
- 5) Change resistor (R3Y10 and R5Y11) value from 475 ohm to 510 ohm.
- 6) Add 1 ohm (0603) resistor to 5V side (anode) of all 8 phases of CPU VR 1 and 2.

- 1) and 2) are to correct Heceta Errata #10-LM93 Sensor Monitoring Issue.
- 3) is a BOM change only required to allow the FRU 1U fan LED to function.
- 4), 5) and 6) are VTT VR regulator changes to improve the VR design reliability/robustness.

## Customer Impact of Change and Recommended Action:

Intel anticipates no customer impact for this change. Evaluate when available.

## Products Affected / Intel Ordering Codes:

**Board Products Table**

Affected Product Code	Pre-Change MM#	Post-Change MM#	Pre-Change TA or AA	Post-Change TA or AA	Pre-Change PBA	Post-Change PBA
BJRDDR1SATABB	879643	879643	C69650-450	C69650-502	C53660-450	C53660-502
SE7520JR2ATAD1	879639	879639	D50238-002	D50238-003	C53660-450	C53660-502

**Reference Documents / Attachments:**

Document:

Location #:

**PCN Revision History:**

Date of Revision:

August 4, 2006

Revision Number:

00

Reason:

Originally Published PCN