



Product Change Notification

112512 - 00

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

A "Mission Critical Application" is any application in which failure of the Intel Product could result, directly or indirectly, in personal injury or death. SHOULD YOU PURCHASE OR USE INTEL'S PRODUCTS FOR ANY SUCH MISSION CRITICAL APPLICATION, YOU SHALL INDEMNIFY AND HOLD INTEL AND ITS SUBSIDIARIES, SUBCONTRACTORS AND AFFILIATES, AND THE DIRECTORS, OFFICERS, AND EMPLOYEES OF EACH, HARMLESS AGAINST ALL CLAIMS COSTS, DAMAGES, AND EXPENSES AND REASONABLE ATTORNEYS' FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PRODUCT LIABILITY, PERSONAL INJURY, OR DEATH ARISING IN ANY WAY OUT OF SUCH MISSION CRITICAL APPLICATION, WHETHER OR NOT INTEL OR ITS SUBCONTRACTOR WAS NEGLIGENT IN THE DESIGN, MANUFACTURE, OR WARNING OF THE INTEL PRODUCT OR ANY OF ITS PARTS.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific/PRC Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

Copyright © Intel Corporation 2013. Other names and brands may be claimed as the property of others.

BunnyPeople, Celeron, Celeron Inside, Centrino, Centrino Inside, Cilk, Core Inside, i960, Intel, the Intel logo, Intel AppUp, Intel Atom, Intel Atom Inside, Intel Core, Intel Inside, Intel Insider, the Intel Inside logo, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel Sponsors of Tomorrow, the Intel Sponsors of Tomorrow logo, Intel StrataFlash, Intel vPro, Intel XScale, InTru, the InTru logo, the InTru Inside logo, InTru soundmark, Itanium, Itanium Inside, MCS, MMX, Moblin, Pentium, Pentium Inside, Puma, skool, the skool logo, Sound Mark, The Creators Project, The Journey Inside, Thunderbolt, vPro Inside, VTune, Xeon, and Xeon Inside are trademarks of Intel Corporation in the U.S. and/or other countries.

Learn how to use Intel Trademarks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



Product Change Notification

Change Notification #: 112512 - 00

Change Title: Intel® Xeon Phi™ Coprocessor 5110P
PCN 112512-00, Product Design
Update System Manageability Controller
(SMC) Code

Date of Publication: August 28, 2013

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Aug 28, 2013
---	--------------

Description of Change to the Customer:

Intel has implemented the following changes on Intel® Xeon Phi™ Coprocessor 5110P:

Update SMC code in manufacturing from 1.7 to 1.11/ Bios 381.1 to 381.2 effective March, 2013.

Update SMC code in manufacturing from 1.11 to 1.14/ Bios 381.2 to 386.2 effective May, 2013.

The SMC code is not supported by the Intel® MPSS package available for download from <http://software.intel.com/mic-developer>.

Customer Impact of Change and Recommended Action:

These changes have been evaluated to ensure there is no quality, reliability, or functional implication to our customers. Intel is not recommending additional qualification of these changes on platforms received from Intel.

Intel will attempt to deplete pre-conversion material before shipping post-conversion material but there is a possibility of Intel shipping mixed inventory. Therefore, customers will need to manage and migrate to the post-conversion material as soon as possible.

Customers must use the micflash tool to update the SMC code as future updates become available with new releases of the Intel® MPSS at <http://software.intel.com/mic-developer>.

Please contact your local Intel Field Sales Rep if you have any further questions about these changes.

Products Affected / Intel Ordering Codes:

SMC code in manufacturing from 1.7 to 1.11/ Bios 381.1 to 381.2

Product Code	MM#	Pre Change TA	Post Change TA
SC5110P	924044	G65758-252	G65758-253

SMC code in manufacturing from 1.11 to 1.14/ Bios 381.2 to 386.2

Product Code	MM#	Pre Change TA	Post Change TA
SC5110P	924044	G65758-253	G65758-254

PCN Revision History:

Date of Revision:
August 28, 2013

Revision Number:
00

Reason:
Originally Published PCN