



Product Change Notification

105955 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 105955 - 00
Change Title: Intel NetStructure® IXB28504XGBE, IXB28504XGBEFS, PCN 105955-00, Product Design, Product Design, Change FRU code to enable inter board communication on separate fabrics as defined in PCI Industrial Computer Manufacturers Group (PICMG) 3.1 specification option 1, option 2 and option 3
Date of Publication: March 02, 2006

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Mar 09, 2006
Date of First Availability of Post-Conversion Material:	Mar 09, 2006

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

The IXB28504XGBE board previously provided only PICMG 3.1 link option 3 while the IXB28504XGBEFS board provided link option 2 & 3 as shown in the table below for their fabric interface. As a result a system with a switch card supporting only link option 2 could not communicate with the IXB28504XGBE board.

Option	Port 0	Port 1	Port 2	Port 3	Description
1	√				One 1000Base-BX
2	√	√			Two 1000Base-BX
3	√	√	√	√	Four 1000Base-BX

This has now been fixed with the FRU code indicating support for PICMG 3.1 link options 1, 2, and 3. Thus allowing inter board communication with cards (i.e. switches) supporting any of these link options. On startup the BMC will indicated to the Shelf Manager that all three link options are now supported on the fabric interface.

Customer Impact of Change and Recommended Action:

No anticipated action required by the IXB28504XGBEFS customers.

For IXB2850XGBE customers the boards may be returned for a FRU update free of charge.

To return a board:

US Distributors: Please fill out the online DRA form, mentioning "FRU update" in the problem description field, at:

<http://www.intel.com/support/motherboards/draform.htm>

Non-US Distributors: Please contact your RMA Call Center to arrange for return and repair.

To avoid shipping expenses, please request the "Intel FedEx account number" when completing the DRA (include this in the problem description section), or when speaking to a representative in the RMA Call Center.

Products Affected / Intel Ordering Codes:

Board Products Table

Affected Product Code	Affected MM#	Pre Change TA #	Post Change TA #	Pre Change CA #	Post Change CA #
IXB28504XGBE	870523	D16984-005	D16984-006	D16985-005	D16985-006
IXB28504XGBEFS	870497	D16719-005	D16719-006	D16720-005	D16720-006

Reference Documents / Attachments:

Document:	Location #:
DRA Form	http://www.intel.com/support/motherboards/draform.htm

PCN Revision History:

Date of Revision:

March 2, 2006

Revision Number:

00

Reason:

Originally Published PCN