



Product Change Notification

106710 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 106710 - 00
Change Title: Intel® 6400 and 6402 Advanced Memory Buffers, PCN 106710-00, Product Design, C-0 to D-1 Stepping Change
Date of Publication: August 31, 2006

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Aug 25, 2006
Date of Qualification Data Availability:	Dec 01, 2006
Date Customer Must be Ready to Receive Post-Conversion Material:	Dec 08, 2006
Date of First Availability of Post-Conversion Material:	Dec 01, 2006

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

The Intel® 6402 Advanced Memory Buffer is converting from the C-0 stepping to the D-1 stepping, in addition the Intel ® 6400 Advanced Memory Buffer is being replaced by the new D-1 stepping of the Intel 6402 Advanced Memory Buffer. Please plan to validate the D-1 stepping of the Intel® 6402 Advanced Memory Buffer. For technical details of the Intel® 6402 Advanced Memory Buffer, please refer to the latest Spec Update available on FDBL.

Customer Impact of Change and Recommended Action:

The D-1 stepping of the Intel® 6402 Advanced Memory Buffer is pin-to-pin compatible with the C-0 stepping and runs at either 533 or 667. The D-1 stepping replaces the C-0 stepping of both the Intel ® 6400 and Intel ® 6402. The D-1 stepping does not require any DIMM modifications. Design changes, however, have been made to the Intel® 6402 Advanced Memory Buffer and will require customers to perform regression testing. Please see the Intel® 6402 Advanced Memory Buffer Specification Update for any BIOS and SPD changes available for the D-1 stepping.

Note - OEMs need to contact the DIMM Vendors for availability of D-1 FB-DIMMs

Products Affected / Intel Ordering Codes:

Intel® 6400 and 6402 Advanced Memory Buffers

Product Code	Pre-Conversion Production Units		Post-Conversion Qualification Samples		Post-Conversion Production Units	
	S-Spec	MM #	QDF #	MM #	S-Spec	MM #
QG6400	S L96G	878953	QN22	885325	SL9XC	886483
QG6402	S L9GF	882114				

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

August 31, 2006

Revision Number:

00

Reason:

Originally Published PCN