



# Product Change Notification

## 104180 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geography location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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# Product Change Notification

**Change Notification #:** 104180 - 00  
**Change Title:** MPCBL0001, PCN 104180-00, FFF,  
**Implemented new ejector handle, new faceplate ejector latches and backing plate insulator. Cosmetic changes on overlay front panel and updated IPMC firmware to V1.4**  
**Date of Publication:** July 15, 2004

**Type of Change Notification:**  
FFF - (Form-Fit-Function)

**Key Characteristics of the Change:**  
Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Jul 26, 2004
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## Description of Change to the Customer:

Form-Fit-Function Changes:

- 1) Changed the dimension of ESD packaging bag to 18" x 18".
- 2) Changed the ATCA(TM) logo to AdvancedTCA(R) logo on all MPCBL0001 Faceplate overlay.
- 3) Changed "RESET" wording on MPCBL0001F04 overlay to capital letter.
- 4) Implemented a new dimension for backing plate insulator.
- 5) Implemented a new dimension for ejector handle and ejector latches.
- 6) Incorporated new IPMC Firmware (Firmware 1.4) which fixes the issues as below:
  - a) Incorrect LED behavior for temperature sensor. (Errata #16)
  - b) Unexpected Thermal trip behavior. (Errata #21)
  - c) Thermal Trip deassertion events are recorded in the System Event Log (SEL) when the board boots up. (Errata #17)
  - d) Changed SDR Sensor type name of "Post Error" to "Sys FW Progress"
  - e) Incorrect Byte 6 data in SDR Type 0x08, 0x09, 0x14.

\*Note\*: All errata# are base on the latest Intel® Netstructure™  
MPCBL0001 High Performance Single Board Computer SPECIFICATION UPDATE -  
June 2004 (Document number: 273941-005)

For Your Information Changes:

- 7) Intel has qualified a new alternate lower profile capacitor (C59/C60).
- 8) Intel has qualified a new alternate capacitor for the Top Mezzanine (C105/C106)  
In future, Intel may use this secondary part in the event of supply shortage.

## Customer Impact of Change and Recommended Action:

For item 1/2/3/7/8

- No functional impact to customers.

For item 4/5

- The design of the ejector handle and latching mechanism has been changed to increase reliability and durability. No customer impact on changes.

For item 6

- Customers are encouraged to update all their current boards with the latest IPMC firmware.

## Products Affected / Intel Ordering Codes:

### Board Products Table

Affected Product Code	Pre-Change MM#	Post-Change MM#	Pre-Change TA or AA	Post-Change TA or AA	Pre-Change PBA	Post-Change PBA
MPCBL0001F04 855965	855965	855965	C55360-008	C55360-009	A97017-207	A97017-208
MPCBL0001N04 855964	855964	855964	C13354-007	C13354-008	C44585-204	C44585-205

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

July 15, 2004

Revision Number:

00

Reason:

Originally Published PCN