



Product Change Notification

Change Notification #: 116321 - 00
Change Title: Intel® Server System R2208WFTZS,
 PCN 116321-00, Product Design, Label,
 CPLD Motherboard Firmware Upgrade
Date of Publication: June 25, 2018

Key Characteristics of the Change:

Product Design, Label

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material*:	July 10, 2018
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* Intel appreciates the customer's desire to receive the latest revision of products. However, Intel manages inventory on a first in first out (FIFO) basis at the MM# level. Subsequently, customer requests for a specific revision of material (below the MM#) will not be supported by Intel.

Description of Change to the Customer:

Intel is implementing the following change on the Intel® Server System R2208WFTZS:

- CPLD Firmware version has been upgraded from 0x0D to 0x0F, the new firmware includes a change into the CPLD code that improves Management Engine (ME) signal access to report more accurate watchdog timeout events.

Customer Impact of Change and Recommended Action:

Intel does not expect any other impact to customers from these changes, but encourages customers to understand the change and determine the impact on their applications. For that reason Intel recommends that the customer perform a standard level of evaluation.

Please contact your local Intel Field Sales Rep if you have any further questions about these changes.

Products Affected / Intel Ordering Codes:

Product Code	Pre Change MM#	Pre Change TA	Pre Change PBA	Post Change MM#	Post Change TA	Post Change PBA
R2208WFTZS	952628	H97440-007	H48104-851 H48104-861	975889	H97440-008	H48104-852 H48104-862

PCN Revision History:

Date of Revision: June 25, 2018	Revision Number: 00	Reason: Originally Published PCN
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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific/PRC Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

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