



# Product Change Notification

## 105173 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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# Product Change Notification

**Change Notification #:** 105173 - 00  
**Change Title:** Intel® NetStructure™ ZT 5524 and MPCBL5524 High-Performance System Master Processor Board, PCN 105173-00, FYI, Rework for boards with BMC 16M flash device  
**Date of Publication:** May 31, 2005

**Type of Change Notification:**  
FYI - (For Your Information)

**Key Characteristics of the Change:**  
Product Design

## Forecasted Key Milestones:

|   |              |
|---|--------------|
| <b>Date Customer Must be Ready to Receive Post-Conversion Material:</b> | Jun 10, 2005 |
|---|--------------|

## Description of Change to the Customer:

A compatibility issue has been discovered with the 16M BMC flash device utilized by the Intel® NetStructure™ ZT 5524 and MPCBL5524 High-Performance System Master Processor Board.

The 16M BMC flash provides an additional address line/pin that needs to be utilized when addressing the increased flash capacity range. However, the ZT 5524 and MPCBL5524 boards do not use memory range above 8M and are not designed to address the additional address line/pin provided by the 16M BMC flash device. This situation creates a potential functional issue when used in certain specific conditions.

As part of Intel's continuing commitment to enhancing product quality and reliability, emergency rework instructions have been created for the existing ZT 5524 and MPCBL5524 boards with 16MB BMC flash device to ground the additional address line/pin which is not utilized.

The rework exercise only affects existing ZT 5524 and MPCBL5524 boards with 16MB BMC flash device that carry the following PBA revision:

ZT5524A1A: A66685-503  
ZT5524A1B: A92326-503  
MPCBL5524A1C: C88604-503  
MPCBL5524A1D: C88454-503

Post-rework PBA revisions will be Cxxxxx-504 and Axxxxx-504 respectively.

## Customer Impact of Change and Recommended Action:

Minimal impact and customers with boards affected have been identified for the rework exercise. This PCN notification serves to document the Cxxxxx-504 and Axxxxx-504 PBA revision changes for customers that are not affected by the rework exercise.

Long term solution will be to revert from current 16M flash device to previous 8M flash device. New boards manufactured by the factory will have the 8M flash device.

## Products Affected / Intel Ordering Codes:

### Board Products Table

| Affected Product Code  | Pre-Change MM# | Post-Change MM# | Pre-Change TA or AA | Post-Change TA or AA | Pre-Change PBA | Post-Change PBA |
|------------------------|----------------|-----------------|---------------------|----------------------|----------------|-----------------|
| MPCBL5524A1C<br>861266 | 861266         | 861266          | C88607-005          | C88607-006           | C88604-503     | C88604-504      |
| MPCBL5524A1D<br>861110 | 861110         | 861110          | C88462-005          | C88462-006           | C88454-503     | C88454-504      |
| ZT5524A1A<br>843737    | 843737         | 843737          | A68370-021          | A68370-022           | A66685-503     | A66685-504      |
| ZT5524A1B<br>843738    | 843738         | 843738          | A68371-020          | A68371-021           | A92326-503     | A92326-504      |

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

May 31, 2005

Revision Number:

00

Reason:

Originally Published PCN