



# Product Change Notification

**Change Notification #:** 116288 - 01  
**Change Title:** Intel® RAID Controller RMS3VC160, PCN 116288-01, Product Design, Label, Order code, Firmware update, bumper and standoff upgrade, add port labels, [Reason for Revision: Add PBA numbers to Products Affected Table](#)  
**Date of Publication:** November 06, 2018

**Key Characteristics of the Change:**  
Product Design, Label, Order Code

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material: *</b>	June 19, 2018
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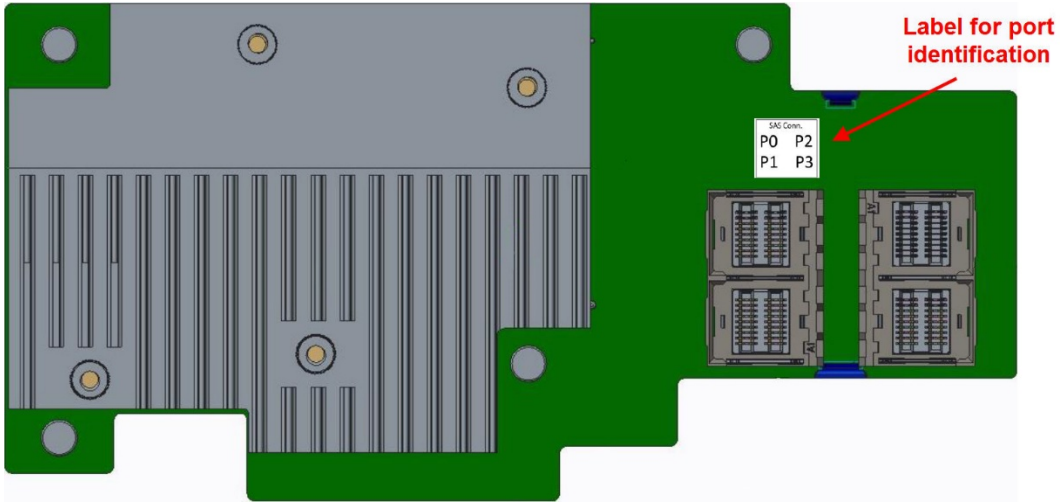
\* Intel appreciates the customer's desire to receive the latest revision of products. However, Intel manages inventory on a first in first out (FIFO) basis at the MM# level. Subsequently, customer requests for a specific revision of material (below the MM#) will not be supported by Intel.

## Description of Change to the Customer:

### [Reason for Revision: Add PBA numbers to Products Affected Table](#)

Intel is implementing the following changes on the Intel® RAID Module RS3VC160:

1. Upgrade the controller's firmware to version 12.00.02.00 (Phase 12).
2. Upgrade the stand-offs (they now include a pull tab to make removal easier)
3. Upgrade the bumper to make this module compatible with the Intel® Server S1200SP family Boards.
4. Add a label to identify the SAS ports (see figure below).



**Customer Impact of Change and Recommended Action:**

Intel does not expect any impact to customers from this change. Customers may need to validate the function and feature change once the post-change system is available.

**Products Affected / Intel Ordering Codes:**

Product Code	MM#	Pre Change TA	Pre Change PBA	Post Change MM#	Post Change TA	Post Change PBA
RMS3VC160	946902	H86850-002	<a href="#">H86852-201</a>	953057	H86850-003	<a href="#">H86852-202</a>

**PCN Revision History:**

Date of Revision:	Revision Number:	Reason:
June 4, 2018	00	Originally Published PCN
<a href="#">November 6, 2018</a>	<a href="#">01</a>	<a href="#">Add PBA numbers to Products Affected Table</a>



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**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

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