



Product Change Notification

103372 - 01

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Product Change Notification

Change Notification #: 103372 - 01
Change Title: SR2300, SR1300, PCN 103372-01, FYI, Class 2; Update Server Board SE7501WV2 from A99386-109 to A99386-110
Date of Publication: June 06, 2003

Type of Change Notification:
FYI - (For Your Information)

Key Characteristics of the Change:
Product Material

Forecasted Key Milestones:

Date of First Availability of Post-Conversion Material:	Jul 18, 2003
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The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

SE7501WV2 hardware changes from A99386-109 to A99386-110(SCSI):
Change ANVIK EEPROM image. Remove unused RC delay on clock driver.
Change fan diode.

Update the baseboard BIOS revision from Production BIOS 1 to Production BIOS 9. Production BIOS 9 contains the following changes:

- 1) Intel(r) Xeon(tm) processor D -1 stepping support.
- 2) Corrected PCIX 5V riser card speed from running at 55MHz to 33MHz.
- 4) Changed default of Q_SYSMGMT_CONTINUE_SERIAL_REDIR token to 0. This prevents the console redirection disable escape sequence from being sent to the keyboard.
- 5) Added code to populate the SSID when the Adaptec RAID option is selected.
- 6) Fixed BIOS LED codes for 3 Memory beep codes.
- 7) Add code to determine if serial ports are disabled when selecting setup items for BIOS Redirection Port and ACPI Redirection Port
- 8) Add code to allow PXE boot from NIC 1 even if NIC 2 is connected.
- 9) Fixed late POST Timeout.
- 10) Fixed 32bit PCI code that returns the PCI IRQ routing information
- 11) Added MCA handler for processors.

- 12) Added debugged error logging of DIMM ordering rules
 - 13) Changed code to lock down the BIOS in the primary partition so it can't be over written.
 - 14) Added a POST screen message to display FSB speed since both 400MHz and 533MHz processors can run in the system.
 - 15) Fixed system ACPI Power State
- Update the baseboard BMC revision from Production BMC 1.09 to Production BMC 1.18. Production BMC 1.18 contains the following changes:
- 1) Watchdog timer expiration
 - 2) SMI Timeout sensor timeout value.
 - 3) DIMM indicator LEDs.
 - 4) The BMC would not allow the upper or lower non-recoverable thresholds of any threshold sensor to be set or read.
 - 5) The power unit fan boost sensor was always stuck in the "Initialization in progress".
 - 6) Interaction between BMC ARPs and the BMC Watchdog timer was incorrect.
 - 7) A problem that prevented BMC LAN communications after a power control failure occurred has been corrected.
 - 8) When the serial channel was enabled and setup for Modem, a LAN session did not receive SOL data packets
 - 9) The BMC would report low voltage events for 3.06 GHz processors.
 - 10) During a BIOS update, the BIOS sometimes does not respond to SMI's, this caused SMI Timeout events to be logged.
 - 11) The BMC would sometimes 'hang' for 10 seconds anytime an attempt to write to, or erase, the BMC flash occurred.
 - 12) The BMC used to log presence events for all DIMMs present every time AC power was applied
 - 13) The power control algorithm has been corrected to avoid causing spurious FRB3 failures.
 - 14) The BMC would occasionally log spurious LAN Leash Lost events.
- Update FRU/SDR version from FRU/SDR 5.3.5. to FRU/SDR 5.5.7 contains the following changes:
- 1) FRU & SDR documentation has been centralized in this file, SdrNotes.txt.
 - 2) The name of the "Proc Hot" sensor was changed to "CPU Thermal Ctrl".
 - 3) The name of the power system redundancy sensor was changed from 'Pwr Redund Lost' to 'Power Redundancy'.
 - 4) Support was added for a new memory cooling fan in the SR2300 chassis, along with a new 5 connector fan board.
 - 5) Support was added for cooling 3.00/3.06 GHz processors in the SR1300 chassis. Maximum ambient temperature is now 30 degrees C.

Customer Impact of Change and Recommended Action:

SE7501WV2 hardware changes from A99386-109 to A99386-110(SCSI):
Change ANVIK EEPROM image. Remove unused RC delay on clock driver.
Change fan diode - Customer needs to be aware of the change; no impact to customer expected.

Update the baseboard BIOS revision from Production BIOS 1 to Production BIOS 9; release notes can be found on support.intel.com for more details - Customer needs to be aware of the change; no impact to customer expected.

Update the baseboard BMC revision from Production BMC 1.09 to Production BMC 1.18; release notes can be found on support.intel.com for more details - Customer needs to be aware of the change; no impact to customer expected.

Update FRU/SDR version from FRU/SDR 5.3.5 to FRU/SDR 5.5.7; release notes can be found on support.intel.com for more details - Customer needs to be aware of the change; no impact to customer expected.

Products Affected / Intel Ordering Codes:

System Products Table

Affected Product Code	Pre-Change MM#	Post-Change MM#	Pre-Change TA	Post-Change TA	Pre-Change Subassembly	Post-Change Subassembly
SE7501WV2SKU02	855630	855630	C22264-009	C22264-010	A99386-109	A99386-110
SWV1USKU07	853491	853491	C32003-004	C32003-005	A99386-109	A99386-110
SWV2USKU07	853492	853492	C32002-003	C32002-004	A99386-109	A99386-110

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

June 5, 2003

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Originally Published PCN

June 5, 2003

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Correction to MM#