



Product Change Notification

107898 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 107898 - 00
Change Title: DQ35MP, PCN 107898-00, Product Material, Fab Spin, new stepping of Marvell PATA chip, and alternate sources for crystal and SPI
Date of Publication: September 05, 2007

Key Characteristics of the Change:

Product Material

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Sep 20, 2007
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Description of Change to the Customer:

Modified the printed circuit board (PCB) in order to increase power plane stability
Stepping change from B1 to B2 on Marvell PATA.
Add second source for clock crystal and Winbond SPI to insure supply.

Customer Impact of Change and Recommended Action:

In Microsoft* Windows* XP*, the pre-existing IDE ATA/ATAPI controller driver will re-enumerate automatically due to the Microsoft* Windows* Plug-and-Play* mechanism. The re-enumeration will impact existing operating system image. User interaction is not required during this process.
These changes have been thoroughly evaluated to ensure that there are no quality, reliability or functional implications to our customers. Intel is not recommending additional qualification of these changes on platforms.

SPI change should not impact BIOS.

Products Affected / Intel Ordering Codes:

Product Code	MM#	Pre Change AA	Post Change AA
BLKDQ35MPE 888803	888803	D82086-702	D82086-800
BOXDQ35MPE 888805	888805	D82086-702	D82086-800

PCN Revision History:

Date of Revision:

September 5, 2007

Revision Number:

00

Reason:

Originally Published PCN