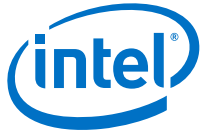


Intel[®] Quark[™] microcontroller D1000

Specification Update

May 2016



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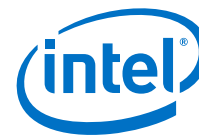
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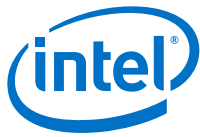
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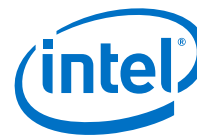
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Revision History

Date	Revision	Description
May 2016	002	Public document published out of sequence to make the following corrections: - Updated Table 1 with Public Specification Numbers - Revised Table 7, Document-Only Changes Item #2
November 2015	001	Initial release

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Introduction

Purpose/Scope/Audience

This document is an update to the specifications listed in the [Affected Documents/Related Documents](#) table that follows. This document is a compilation of [Errata](#), [Specification Changes](#), [Specification Clarifications](#), and [Document-Only Changes](#). It is intended for hardware and software system designers and manufacturers as well as developers of applications, operating systems, or tools.

This document may also contain information that was not previously published.

Table 1. Affected Documents/Related Documents

Title	Number
Intel® Quark™ microcontroller D1000 Datasheet	332910
Intel® Quark™ microcontroller D1000 User Guide	332912
Intel® Quark™ microcontroller D1000 Programmer's Reference Manual	332913

Conventions and Terminology

Note: [Errata](#) remain in the Specification Update throughout the product's lifecycle, or until a particular stepping is no longer commercially available. Under these circumstances, errata removed from the Specification Update are archived and available upon request. [Specification Changes](#), [Specification Clarifications](#) and [Document-Only Changes](#) are removed from the Specification Update when the appropriate changes are made to the appropriate product specification or user documentation (datasheets, manuals, etc.).

Table 2. Conventions and Terminology (Sheet 1 of 2)

Term	Definition
Document-Only Changes	Document-Only Changes are changes to an Intel Parent Specification that result in changes only to an Intel customer document but no changes to a specification or to a parameter for an Intel product. An example of a document-only change is the correction of a typographical error.
Errata (plural) Erratum (singular)	Errata are design defects or errors. These may cause the Product Name's behavior to deviate from published specifications. Hardware and software designed to be used with any given stepping must assume that all errata documented for that stepping are present on all devices.
Parent Specification	A parent specification is a top-level specification from which other documents can be derived, depending on the product or platform. Typically, a parent specification includes a product's pinout, architectural overview, device operation, hardware interface, or electrical specifications. Examples of parent specifications include the following: Datasheet, Developer's Manual, Technical Product Specification (also known as "TPS"). The derived documents may be used for purposes other than that for which the parent specification is used.

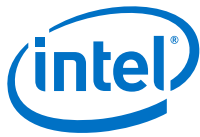
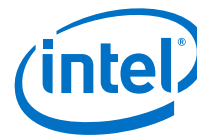


Table 2. Conventions and Terminology (Sheet 2 of 2)

Term	Definition
Specification Changes	Specification Changes are the result of adding, removing, or changing a feature, after which an Intel product subsequently operates differently than specified in an Intel Parent Specification, but typically the customer does not have to do anything to achieve proper device functionality as a result of Intel adding, removing, or changing a feature.
Specification Clarifications	Specification Clarifications are changes to a document that arise when an Intel Parent Specification must be reworded so that the specification is either more clear or not in conflict with another specification.



Summary Tables of Current Product Issue Activity

Table 4 through Table 7 indicate the [Errata](#), [Specification Changes](#), [Specification Clarifications](#), or [Document-Only Changes](#) that apply to the Product Name product. Intel may fix some of the [Errata](#) in a future stepping of the component as noted in [Table 3](#) or account for the other outstanding issues through [Specification Changes](#), [Specification Clarifications](#), or [Document-Only Changes](#). Table 4 through Table 7 use the codes listed in [Table 3](#).

Table 3. Codes Used in Summary Tables

Code	Column	Definition
X	Stepping	Indicates either that, for the stepping/revision listed, - an erratum eXists and is not yet fixed - a specification change or specification clarification applies
No mark or blank	Stepping	Indicates either that, for the stepping/revision listed, - an erratum is fixed - a specification change or specification clarification does not apply
Plan Fix	Status	This erratum may be fixed in a future stepping/revision.
Fixed	Status	This erratum has been previously fixed.
No Fix	Status	There are no plans to fix this erratum.
A change bar to the left of a table row indicates an item that is either new or modified from the previous version of the Specification Update document.		

Table 4. Errata

No.	Stepping / Revision			Status	Errata
	#	#	#		
					None for this revision of this Specification Update.

Table 5. Specification Changes

No.	Stepping / Revision			Specification Changes
	#	#	#	
				None for this revision of this Specification Update.

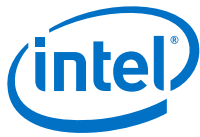
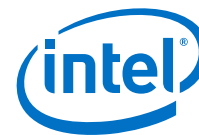


Table 6. Specification Clarifications

No.	Stepping / Revision			Specification Clarifications
	#	#	#	
				None for this revision of this Specification Update.

Table 7. Document-Only Changes

No.	Document Title	Rev.	Document-Only Changes
1	Intel® Quark™ microcontroller D1000 Datasheet	0.6	Correction has been incorporated into the 332910 Intel® Quark™ microcontroller D1000 Datasheet.
2	Intel® Quark™ microcontroller D1000 Programmer's Reference Manual	1.4	Correction has been incorporated into the 332913 Intel® Quark™ microcontroller D1000 Programmer's Reference Manual.



General Product Information

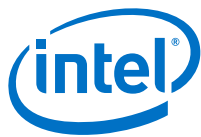
The Intel® Quark™ microcontroller D1000 can be identified by the following register contents;

Table 8. Identification Information

Part Number	Stepping	Notes
Contact your Intel sales representative.	B1	None

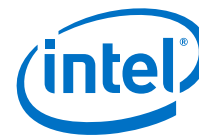
The Intel® Quark™ microcontroller D1000 stepping can be identified by the following component markings;

Stepping	SKU	Identifier	MM #
B1	1	Intel® Quark™ microcontroller D1000, T&R	938719



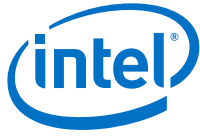
Errata

There are no new Errata in this Specification Update revision.



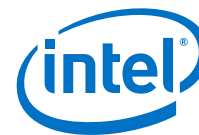
Specification Changes

There are no new Specification Changes in this Specification Update revision.



Specification Clarifications

There are no new Specification Clarification in this Specification Update revision.



Document-Only Changes

1. **Correction on the Max Value for the System Power Consumption Table**

Correction has been incorporated into the *Intel® Quark™ microcontroller D1000 Datasheet*, Doc# 332910.

2. **Correction on the INC - Increment by 1 Table**

Correction has been incorporated into the *Intel® Quark™ microcontroller D1000 Programmer's Reference Manual*, Doc# 332913.

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